

L Number	Hits	Search Text	DB	Time stamp
1	1	"6351142"	USPAT; US-PGPUB	2002/09/15 16:13
2	115	703/20.ccls.	USPAT; US-PGPUB	2002/09/15 16:16
3	5	703/20.ccls. and (datapath or 'data path')	USPAT; US-PGPUB	2002/09/15 16:16
-	1384	datapath	USPAT; US-PGPUB	2002/09/15 13:38
-	1081	datapath and register	USPAT; US-PGPUB	2002/09/13 14:13
-	1139	datapath and register	USPAT; US-PGPUB	2002/09/13 14:15
-	671	(datapath and register) and (model\$3 or simulat\$4 or emulat\$4 or synthesiz\$3)	USPAT; US-PGPUB	2002/09/13 14:15
-	706	(datapath and register) and (model\$3 or simulat\$4 or emulat\$4 or synthesiz\$3)	USPAT; US-PGPUB;	2002/09/13 14:15
-	695	((datapath and register) and (model\$3 or simulat\$4 or emulat\$4 or synthesiz\$3)) and (processor or microprocessor or IC or 'integrated circuit' or ASIC or SOC or VLIW)	USPAT; US-PGPUB; IBM_TDB	2002/09/13 14:17
-	690	((datapath and register) and (model\$3 or simulat\$4 or emulat\$4 or synthesiz\$3)) and (processor or microprocessor or IC or 'integrated circuit' or ASIC or SOC or VLIW)) and (function\$5 or macrocell or instruction)	USPAT; US-PGPUB; IBM_TDB	2002/09/13 14:18
-	5	((datapath and register) and (model\$3 or simulat\$4 or emulat\$4 or synthesiz\$3)) and (processor or microprocessor or IC or 'integrated circuit' or ASIC or SOC or VLIW)) and (function\$5 or macrocell or instruction)) and parallel) and cliques	USPAT; US-PGPUB; IBM_TDB	2002/09/13 15:29
-	10	"5513123"	USPAT; US-PGPUB; IBM_TDB	2002/09/13 14:27
-	5	((datapath and register) and (model\$3 or simulat\$4 or emulat\$4 or synthesiz\$3)) and (processor or microprocessor or IC or 'integrated circuit' or ASIC or SOC or VLIW)) and (function\$5 or macrocell or instruction)) and parallel) and clique	USPAT; US-PGPUB; IBM_TDB	2002/09/13 15:29
-	581	((datapath and register) and (model\$3 or simulat\$4 or emulat\$4 or synthesiz\$3)) and (processor or microprocessor or IC or 'integrated circuit' or ASIC or SOC or VLIW)) and (function\$5 or macrocell or instruction)) and parallel	USPAT; US-PGPUB; IBM_TDB	2002/09/13 15:39
-	269	((datapath and register) and (model\$3, or simulat\$4 or emulat\$4 or synthesiz\$3)) and (processor or microprocessor or IC or 'integrated circuit' or ASIC or SOC or VLIW)) and (function\$5 or macrocell or instruction)) and parallel) and node	USPAT; US-PGPUB; IBM_TDB	2002/09/13 15:40
-	185	((datapath and register) and (model\$3 or simulat\$4 or emulat\$4 or synthesiz\$3)) and (processor or microprocessor or IC or 'integrated circuit' or ASIC or SOC or VLIW)) and (function\$5 or macrocell or instruction)) and parallel) and node) not camera	USPAT; US-PGPUB; IBM_TDB	2002/09/13 15:40
-	10	"5513123"	USPAT; US-PGPUB	2002/09/15 15:18